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Bib Data Sheet

CONFIRMATION NO. 7450

SERIAL NUMBER 10/529,962	FILING DATE 03/31/2005 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. BE02 0028 US
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** CONTINUING DATA *****

This application is a 371 of PCT/IB03/04303 09/30/2003 CL

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 02079084.6 10/03/2002 CL

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature Initials	STATE OR COUNTRY NETHERLANDS	SHEETS DRAWING 3	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 1
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ADDRESS

24738

PHILIPS ELECTRONICS NORTH AMERICA CORPORATION

INTELLECTUAL PROPERTY & STANDARDS

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SAN JOSE , CA

95131

TITLE

Method of manufacturing a semiconductor device with field isolation regions consisting of grooves filled with isolation material

FILING FEE RECEIVED 900	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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